



深圳市银鹏威电子有限公司

SHENZHEN YINPENGWEI ELECTROCNIC CO. LTD

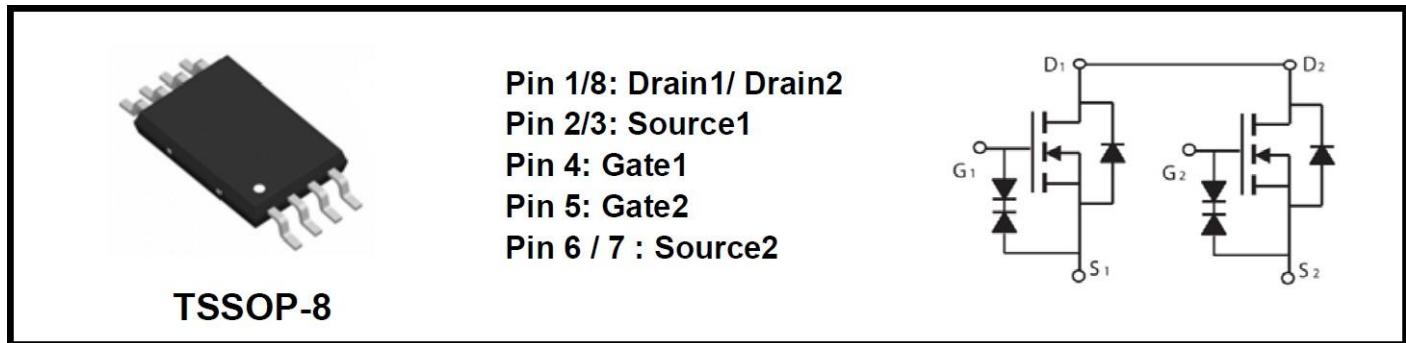
PW8810A

Dual N-Channel Enhancement-Mode MOSFET(20V, 10A)

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{D(on)} (m-ohm) TYP
20V	10A	10.5@ VGS = 4.5V, ID=1A
		14@ VGS = 2.5V, ID=1A

◆ Features

1. High density cell design for ultra low On-Resistance.
2. Advanced trench process technology.
3. RoHS Compliant.



◆ Absolute Maximum Ratings($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	± 10	V
I _D	Drain Current (Continuous) ^a	10	A
I _{DM}	Drain Current (Pulsed) ^b	32	A
P _D	Total Power Dissipation @TA=25°C	2	W
T _j , T _{stg}	Operating Junction and Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance Junction to Ambient (PCB mounted) ^c	100	°C/W



a:Fused current that based on wire numbers and diameter

b:Repetitive Rating: Pulse width limited by the maximum junction temperature

c:1-in² 2oz Cu PCB board

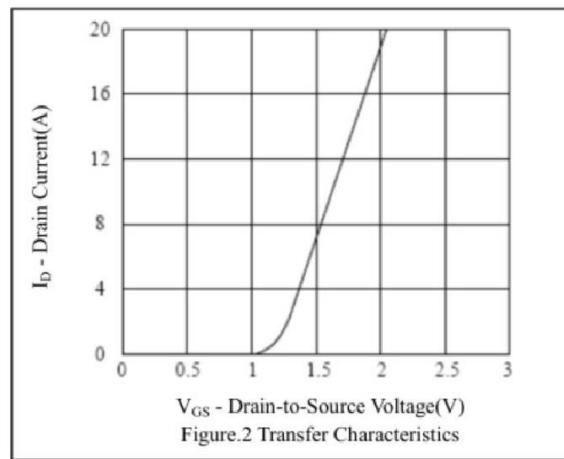
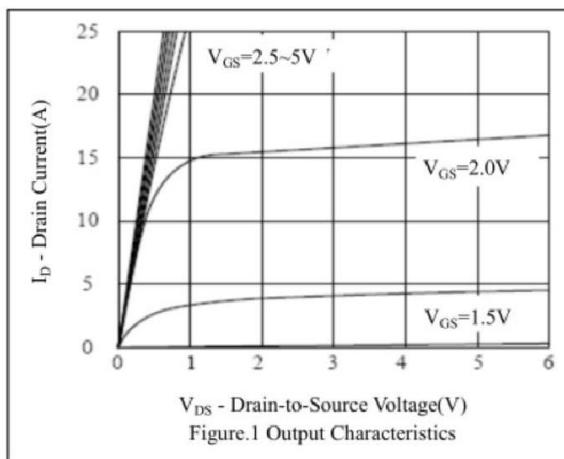
◆ Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
• On Characteristics						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	0.5	-	1.1	V
$R_{\text{DS(on)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=1\text{A}$		10.5	11	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_{\text{D}}=1\text{A}$	-	14	18	
• Dynamic Characteristics^d						
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	-	950	-	pF
C_{oss}	Output Capacitance		-	450	-	
C_{rss}	Reverse Transfer Capacitance		-	135	-	
• Switching Characteristics^d						
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=3\text{A}$, $V_{\text{GS}}=4.5\text{V}$	-	9.2	-	nC
Q_{gs}	Gate-Source Charge		-	2.7	-	
Q_{gd}	Gate-Drain Charge		-	3.7	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=10\text{V}$, $I_{\text{D}}=1\text{A}$, $V_{\text{GEN}}=4.5\text{V}$, $R_{\text{G}}=6\Omega$	-	10	-	nS
t_r	Turn-on Rise Time		-	14	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	39	-	
t_f	Turn-off Fall Time		-	26	-	
• Drain-Source Diode Characteristics						
I_s	Maximum Diode Forward Current		-	-	1.7	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1.7\text{A}$	-	-	1.2	V

Note: Pulse Test: Pulse Width $\leq 300\text{us}$, Duty Cycle $\leq 2\%$

d: Guaranteed by design: not subject to production testing

◆ Characteristics Curve



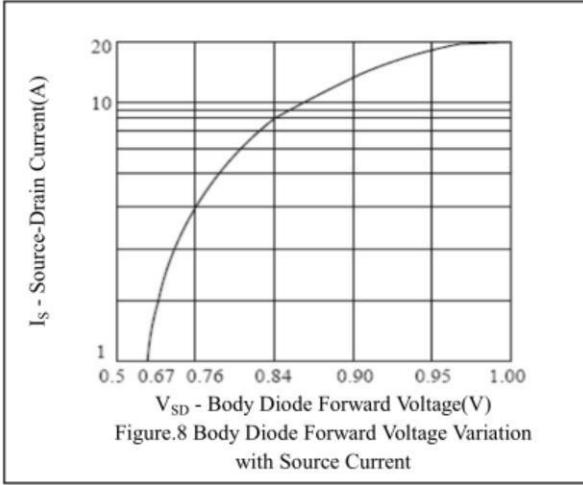
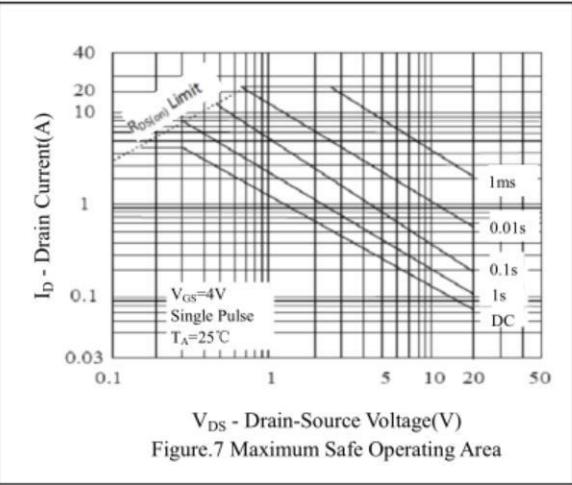
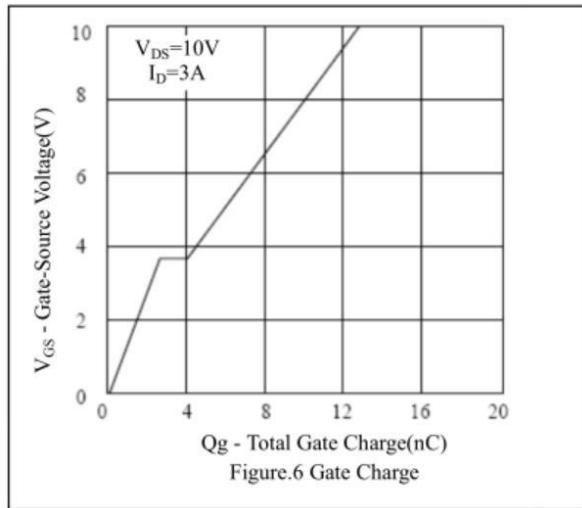
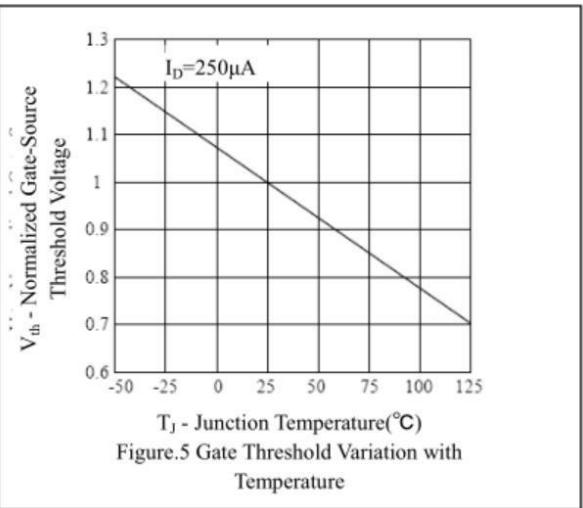
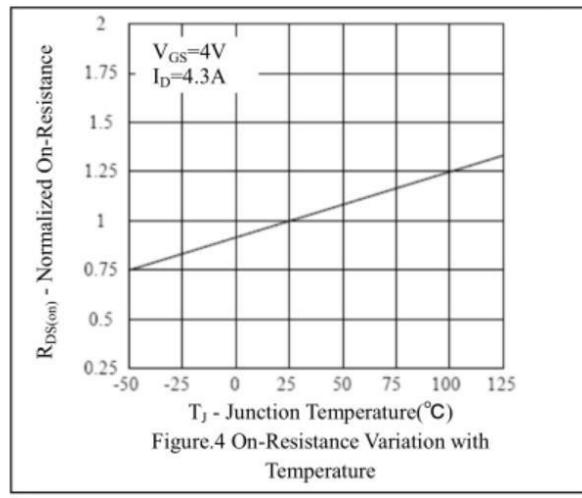
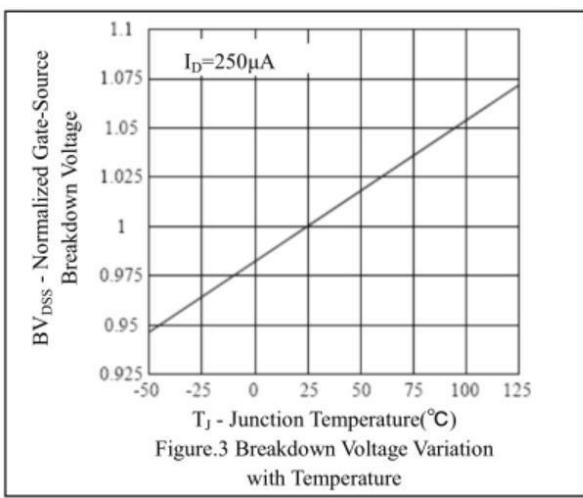


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