

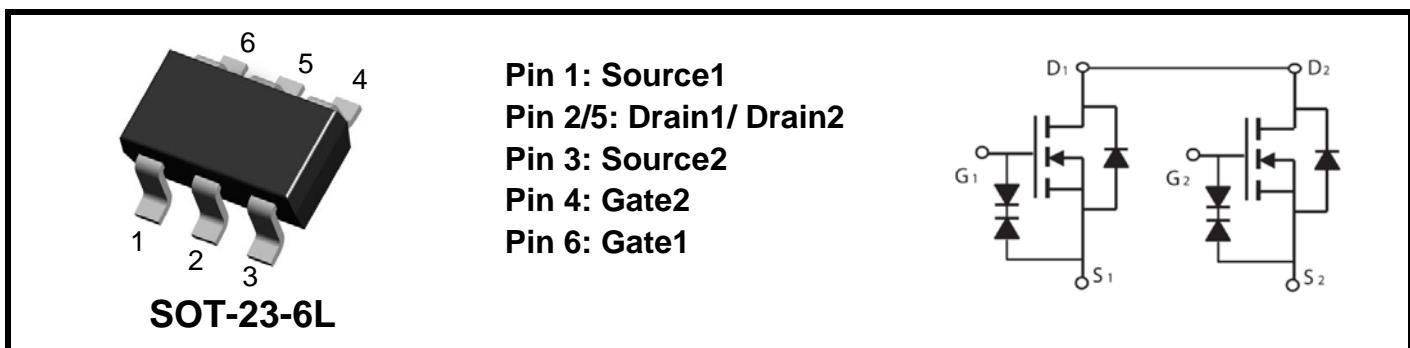


Dual N-Channel Enhancement-Mode MOSFET(20V, 6A)

PRODUCT SUMMARY		
V _{DSS}	I _D	R _{D(on)} (m-ohm) Max
20V	6A	14 @ V _{GS} = 4.5V, I _D =4.5A
		18 @ V _{GS} = 2.5V, I _D =3.5A

◆ Features

1. High density cell design for ultra low On-Resistance.
2. Advanced trench process technology.
3. RoHS Compliant.
4. ESD Protected

◆ Absolute Maximum Ratings($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D	Drain Current (Continuous) ^a	6	A
I _{DM}	Drain Current (Pulsed) ^b	25	A
P _D	Total Power Dissipation @ TA=25°C	1.5	W
T _j , T _{stg}	Operating Junction and Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance Junction to Ambient (PCB mounted) ^c	100	°C/W

a:Fused current that based on wire numbers and diameter

b:Repetitive Rating: Pulse width limited by the maximum junction temperature

c:1-in² 2oz Cu PCB board



◆ **Electrical Characteristics** ($T_A=25^\circ C$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=15V, V_{GS}=0V$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	± 10	μA
• On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	-	1.1	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=4.5V, I_D=4.5A$	-	10.5	14	$m\Omega$
		$V_{GS}=2.5V, I_D=3.5A$	-	14	18	
• Dynamic Characteristics^d						
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	-	950	-	pF
C_{oss}	Output Capacitance		-	450	-	
C_{rss}	Reverse Transfer Capacitance		-	135	-	
• Switching Characteristics^d						
Q_g	Total Gate Charge	$V_{DS}=10V, I_D=3A, V_{GS}=4.5V$	-	9.2	-	nC
Q_{gs}	Gate-Source Charge		-	2.7	-	
Q_{gd}	Gate-Drain Charge		-	3.7	-	
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=10V, I_D=1A, V_{GEN}=4.5V, R_G=6\Omega$	-	10	-	nS
t_r	Turn-on Rise Time		-	14	-	
$t_{d(off)}$	Turn-off Delay Time		-	39	-	
t_f	Turn-off Fall Time		-	26	-	
• Drain-Source Diode Characteristics						
I_S	Maximum Diode Forward Current		-	-	1.7	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS}=0V, I_S=1.7A$	-	-	1.2	V

Note: Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

d: Guaranteed by design: not subject to production testing



◆ Characteristics Curve

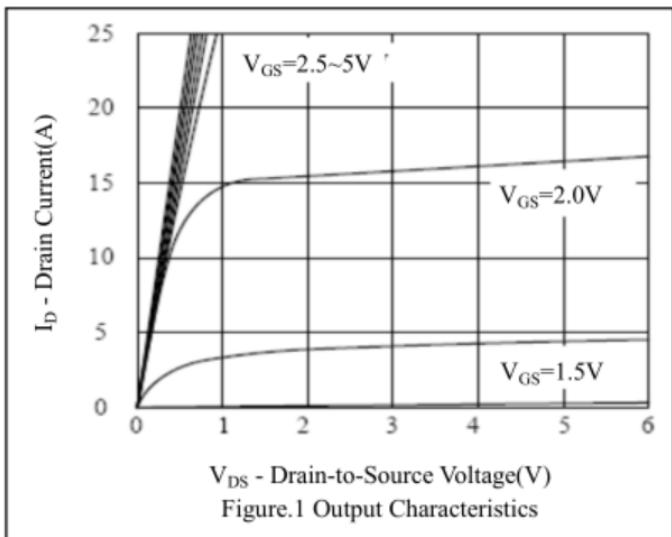


Figure.1 Output Characteristics

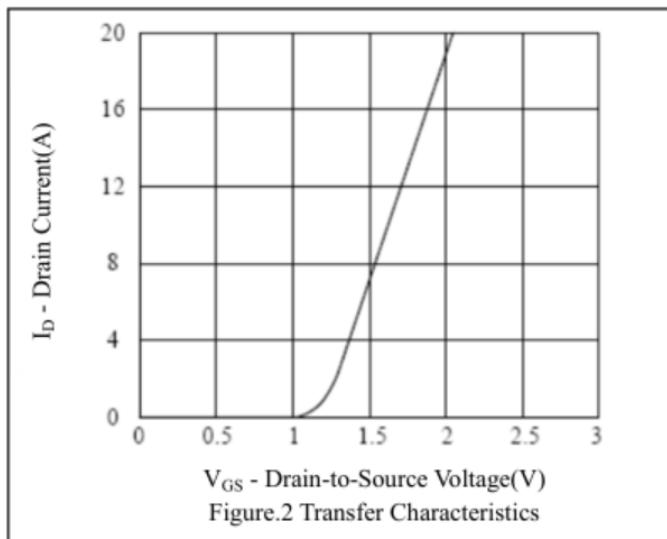


Figure.2 Transfer Characteristics

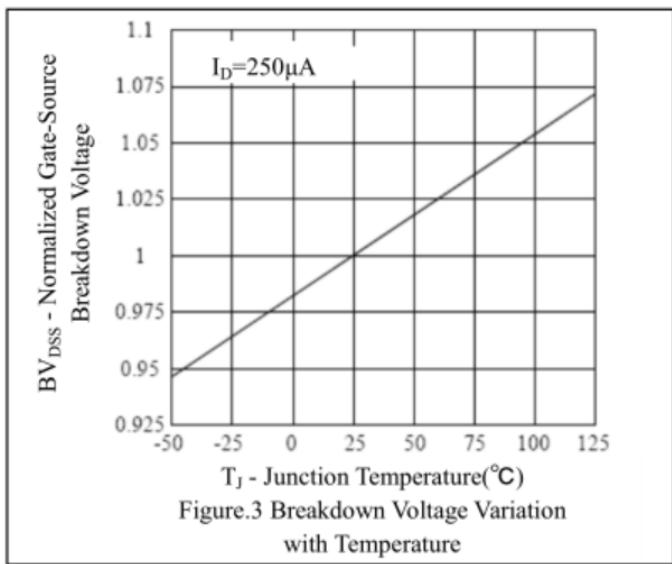


Figure.3 Breakdown Voltage Variation with Temperature

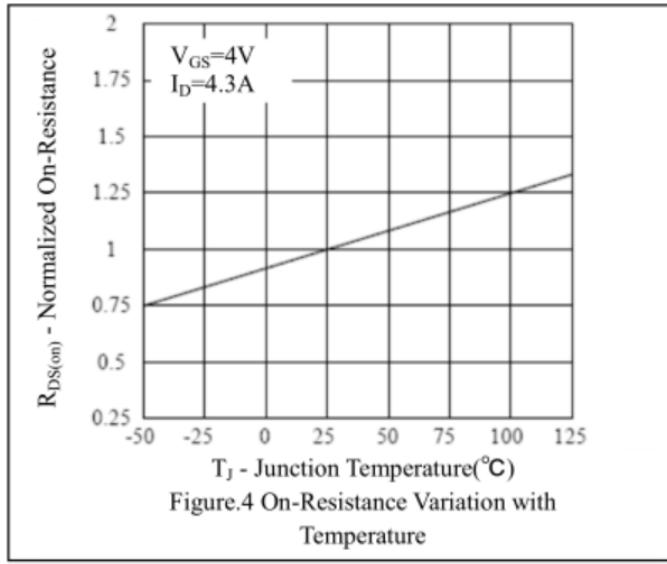


Figure.4 On-Resistance Variation with Temperature

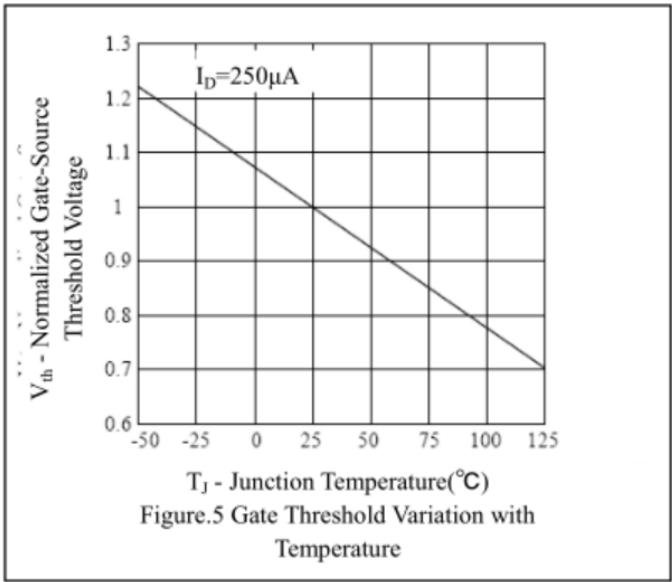


Figure.5 Gate Threshold Variation with Temperature

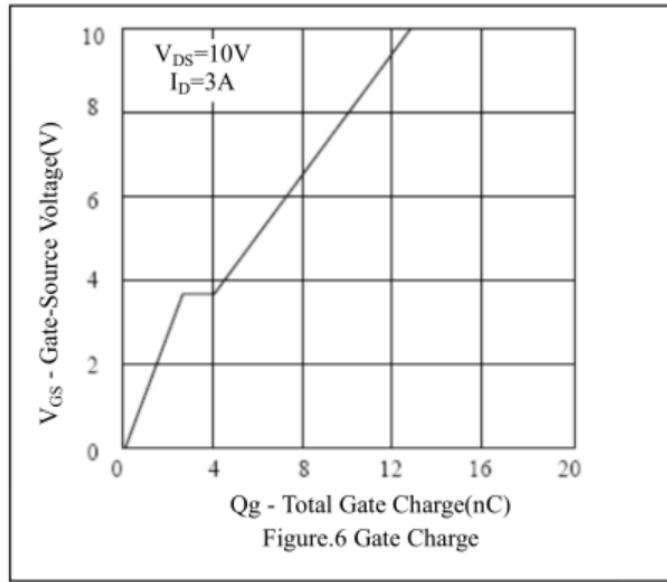


Figure.6 Gate Charge



◆ Characteristics Curve

